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| Application Number | |
| Filing Date | August 17, 2001 |
| First Named Inventor | Gerard Chauvel, et al. |
| Group Art Unit | 2681 |
| Examiner Name | |
| Attorney Docket No. | TI-31362 |

U.S. PATENT DOCUMENTS

| Exam. Initials* | Cite No. ¹ | U.S. Patent Document | | Name of Patentee or Applicant of Cited Doc. | Date of Pub. of Cited Doc. (mm-dd-yyyy) | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

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| Exam. Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| ZH | CA | F. Bellosa, <i>OS-Directed Throttling of Processor Activity for Dynamic Power Management</i> , Technical Report, 'Online! June 1999, pgs. 1-5 (URL .http://www4.informatik.uni-erlangen.de/TR/pdf/TR-14-99-03.pdf); retrieved on Internet on Aug. 12, 2003. | |
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| Examiner Signature | Lisa Hashem | Date Considered | June 1, 2004 |
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